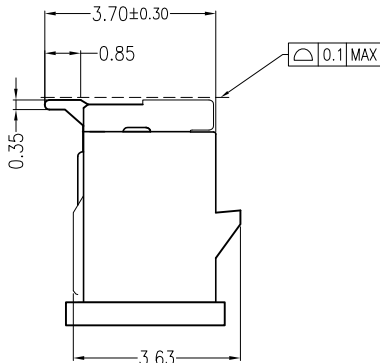
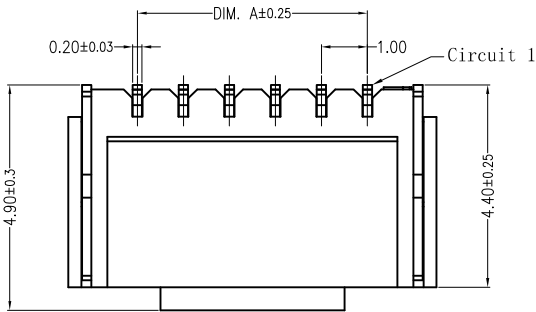
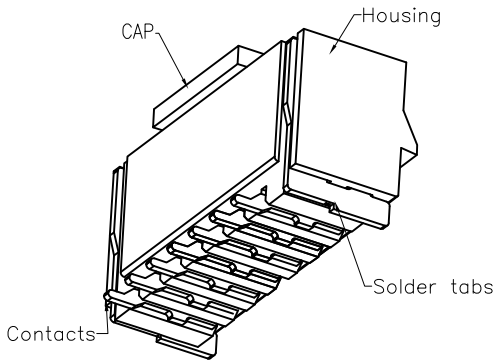
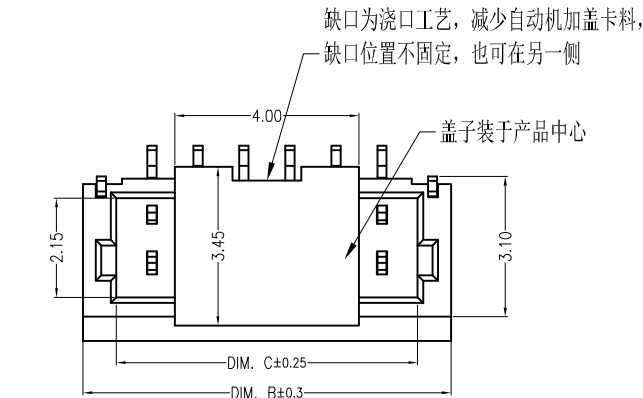


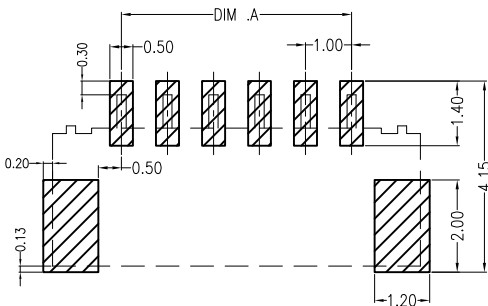
REV.	ECN NO.	LOCAS.	DESCRIPTION	DATE	DESIGN
A			NEW DRAWING		



Specifications:
Contact resistance:≤20mΩ
Insulation resistance:≥100MΩ
Rated voltage:50V AC DC
Rated current:1.0A AC DC $\Delta 2$
Withstand voltage:500V AC/minute
Temperature range:-25℃~ +105℃
Housing:LCP UL94V-0,Natural
CAP:LCP UL94V-0,Natural
Contacts:Phosphor bronze(Matte sn/Gold plating)
Solder tabs:Phosphor bronze/Matte sn



Circuit	Dimensions (mm)		
	DIM. A	DIM. B	DIM. C
02	1.00	4.00	2.45
03	2.00	5.00	3.45
04	3.00	6.00	4.45
05	4.00	7.00	5.45
06	5.00	8.00	6.45
07	6.00	9.00	7.45
08	7.00	10.00	8.45
09	8.00	11.00	9.45
10	9.00	12.00	10.45
*11	10.00	13.00	11.45
*12	11.00	14.00	12.45
*13	12.00	15.00	13.45
*14	13.00	16.00	14.45
*15	14.00	17.00	15.45
*16	15.00	18.00	16.45
*20	19.00	22.00	20.45



Recommend PCB Layout

DESIGN:	Xiping Huang	 广东国连电子有限公司 GUANGDONG GUOLIAN ELECTRONICS CO.,LTD			
CHECK:	Ke Tian				
APPROVED:	Youquan Xu	TITLE: 1.0立贴带扣			
DIM	TOL	DIM	TOL	PART NO. 1.0-PT-NPB	
X.	±0.25	X°	±3.0°	REV. A SCALE: N/A UNIT: mm SHEET: 1/1 	
X. X	±0.20	X. X°			
X. XX	±0.15	X. XX°			
X. XXX		X. XXX°			