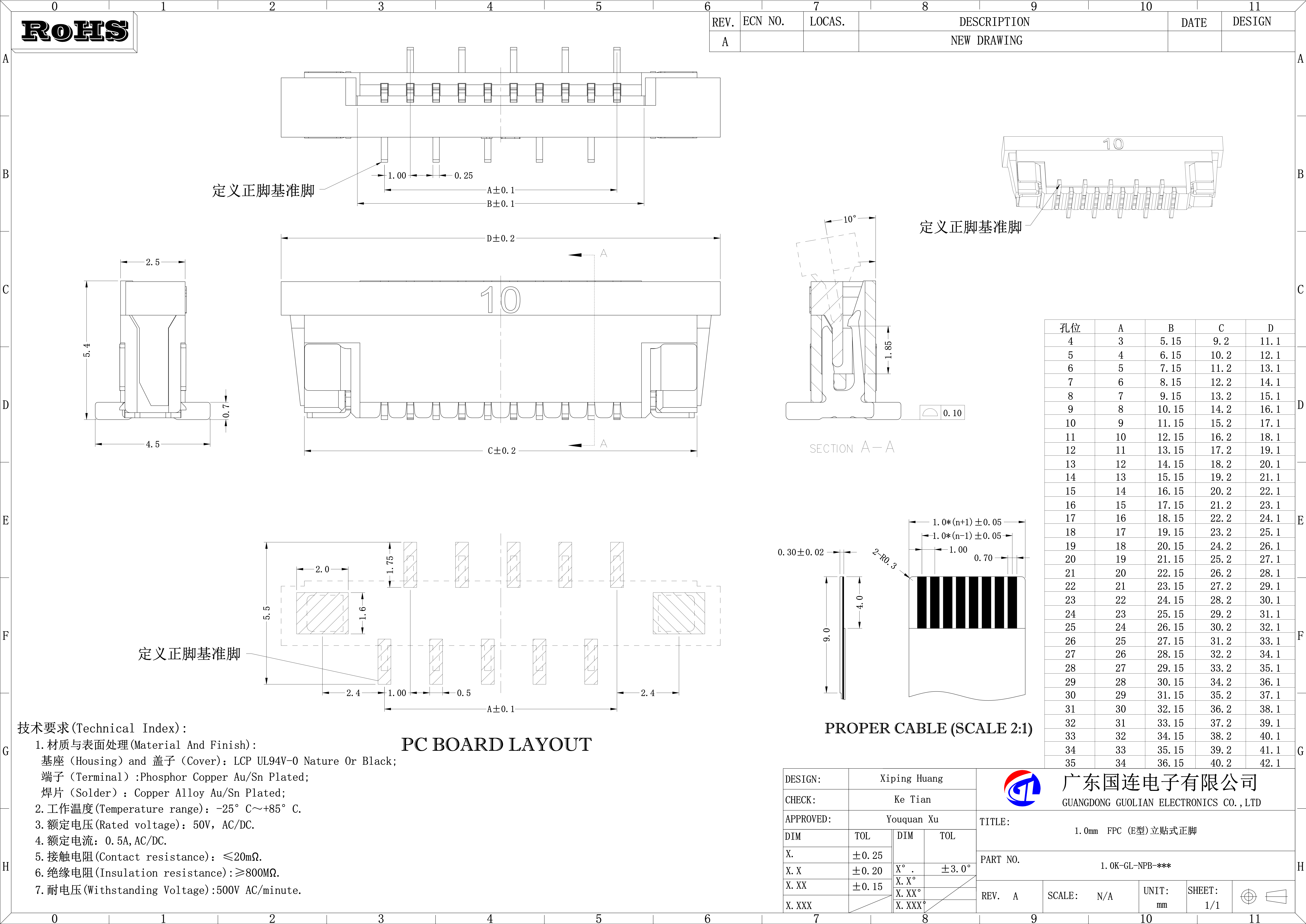




REV.

ECN NO.

LOCAS.

DESCRIPTION

DATE

DESIGN

A

NEW DRAWING

01234567891011

RoHS

A

B

C

D

E

F

G

H

定义正脚基准脚

1.00

0.25

A±0.1

B±0.1

2.5

5.4

4.5

0.7

10

D±0.2

C±0.2

A

A

定义正脚基准脚

10

10°

1.85

0.10

SECTION A—A

定义正脚基准脚

2.0

1.75

1.6

2.4

1.00

0.5

A±0.1

2.4

0.30±0.02

9.0

4.0

2-R0.3

1.0*(n+1) ±0.05

1.0*(n-1) ±0.05

1.00

0.70

技术要求(Technical Index):
1. 材质与表面处理(Material And Finish):
基座(Housing)和盖子(Cover): LCP UL94V-0 Nature Or Black;
端子(Terminal):Phosphor Copper Au/Sn Plated;
焊片(Solder): Copper Alloy Au/Sn Plated;
2. 工作温度(Temperature range):-25° C~+85° C.
3. 额定电压(Rated voltage): 50V, AC/DC.
4. 额定电流: 0.5A, AC/DC.
5. 接触电阻(Contact resistance): ≤20mΩ.
6. 绝缘电阻(Insulation resistance): ≥800MΩ.
7. 耐电压(Withstanding Voltage):500V AC/minute.

PC BOARD LAYOUT

DESIGN:

Xiping Huang

CHECK:

Ke Tian

APPROVED:

Youquan Xu

DIM

TOL

DIM

TOL

X.

±0.25

X°

±3.0°

X.X

±0.20

X.X°

X.XX

±0.15

X.XX°

X.XXX

X.XXX°

TITLE:

1.0mm FPC (E型)立贴式正脚

PART NO.

1.0K-GL-NPB-***

REV.

A

SCALE:

N/A

UNIT:

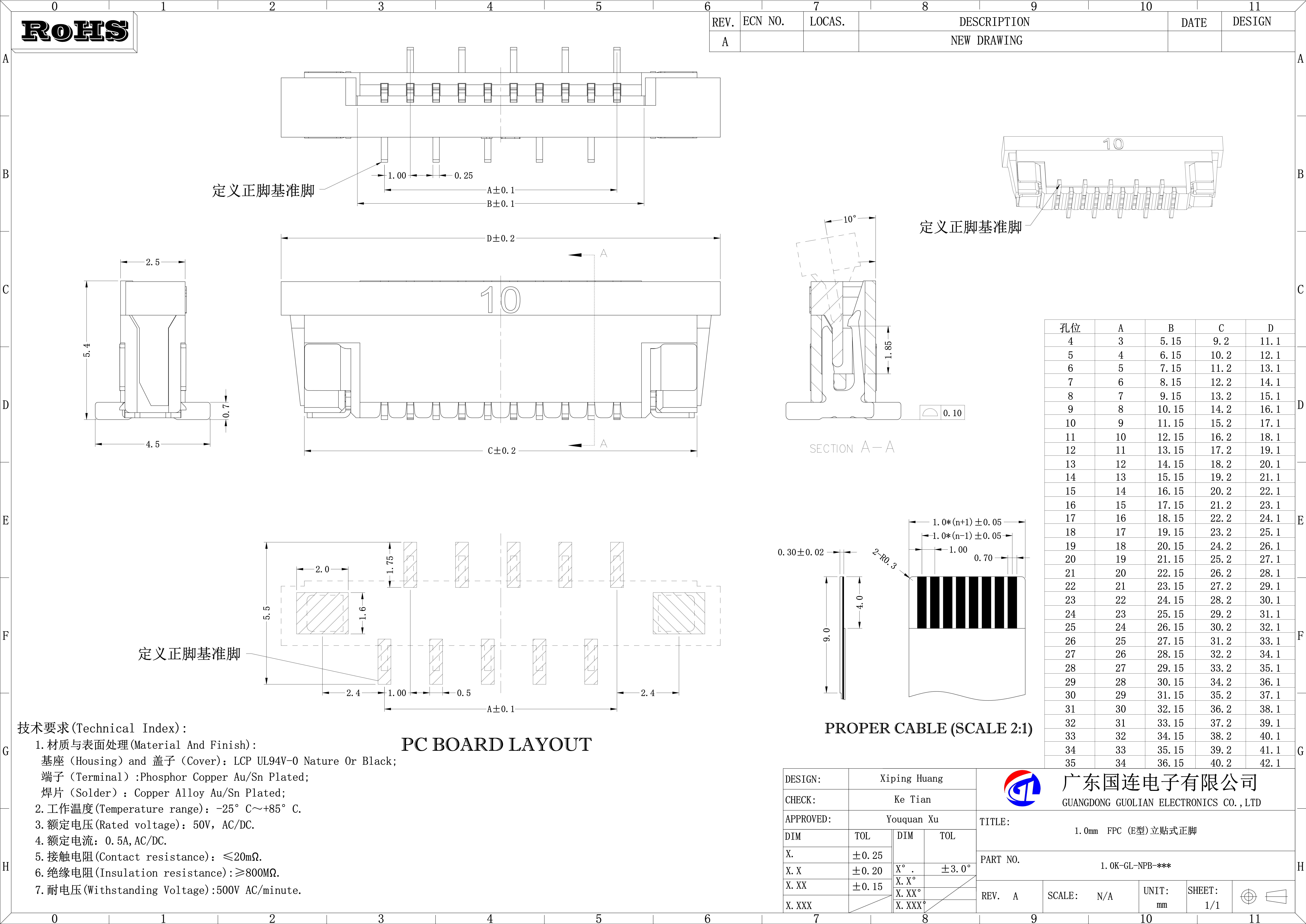
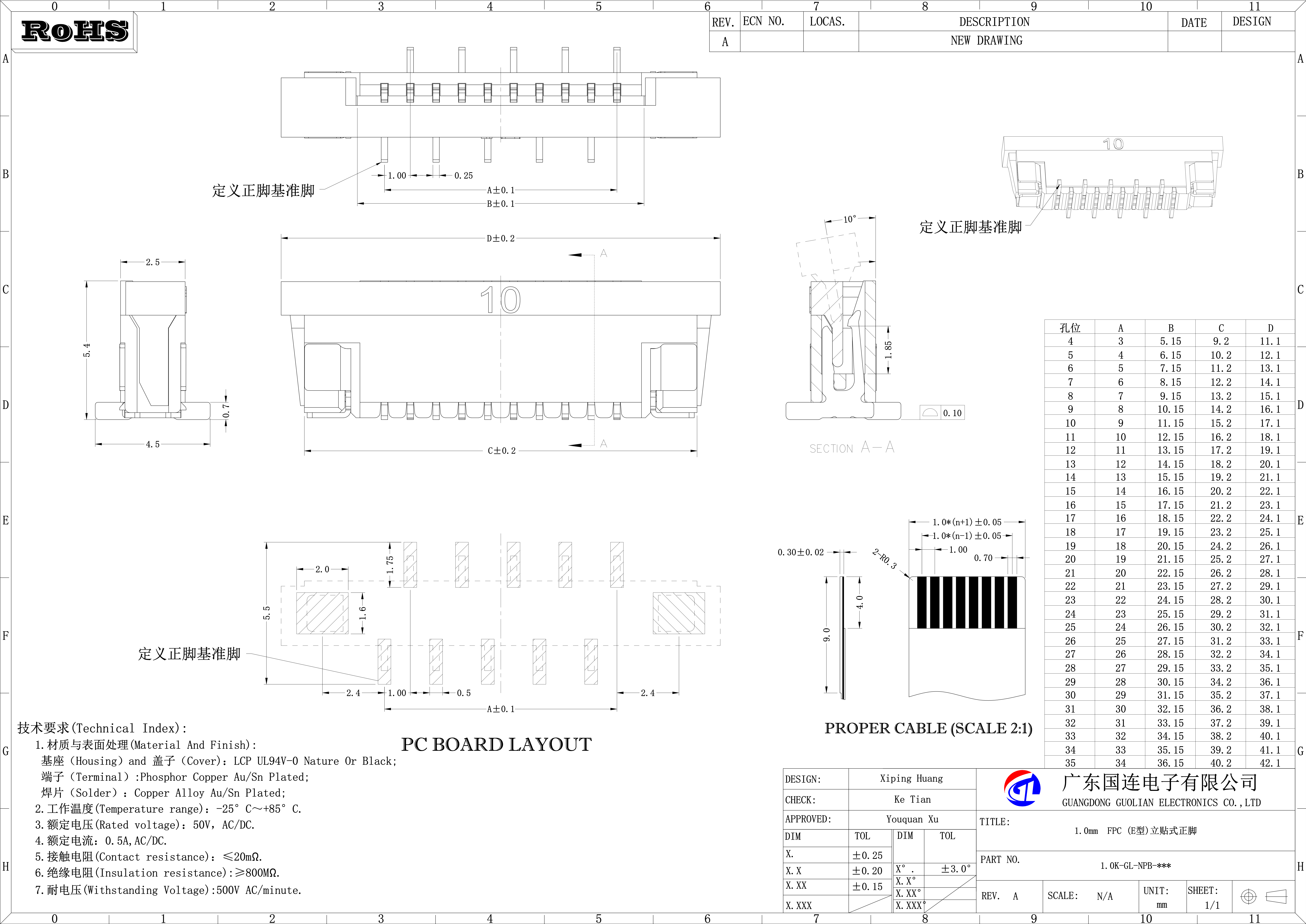
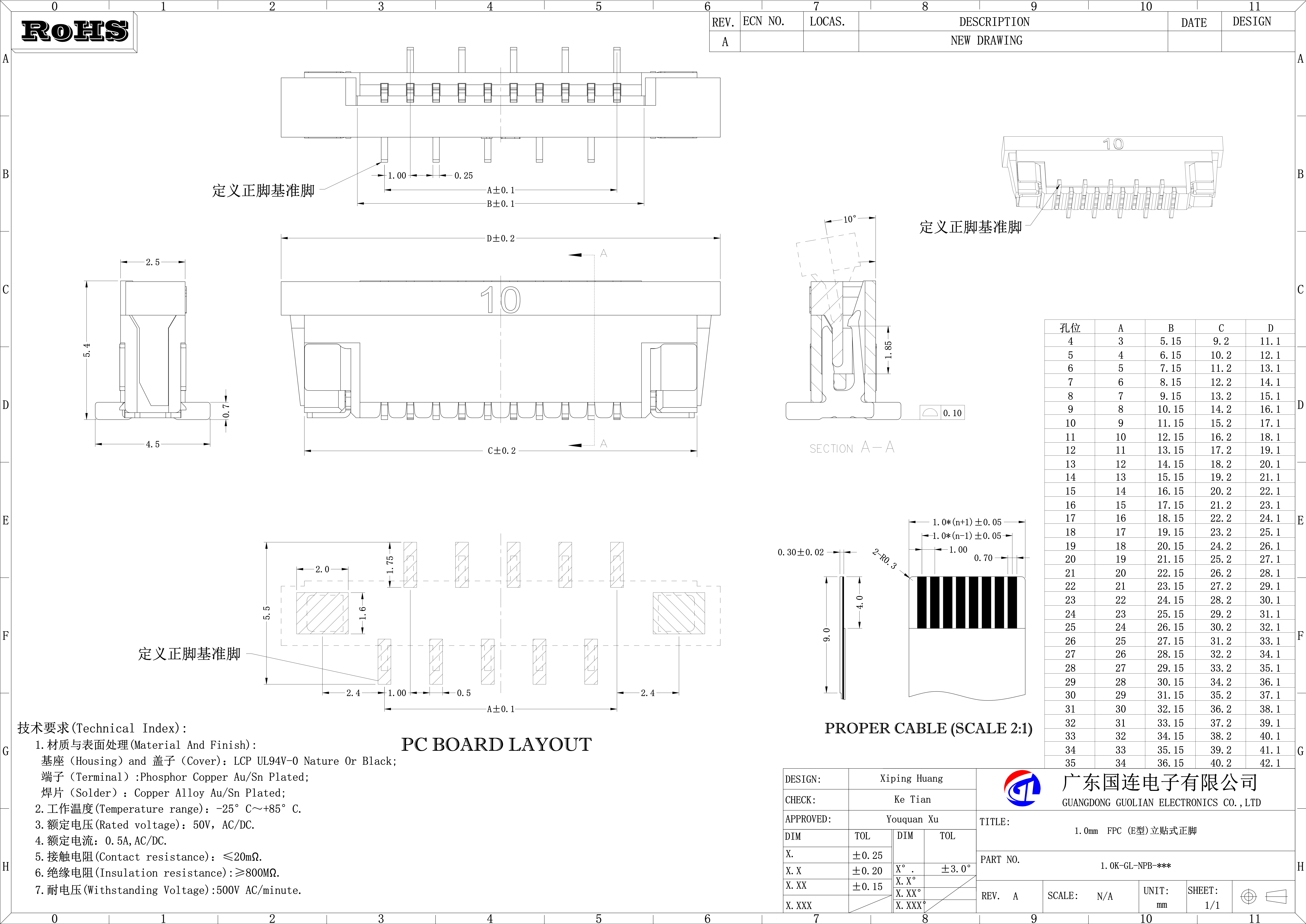
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SHEET:

1/1

广东国连电子有限公司
GUANGDONG GUOLIAN ELECTRONICS CO.,LTD

01234567891011

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